

# **Snap Shot**

**November 6, 2003**

**System architecture review**

**Statement of Work Released**

**Non-Invasive Testing Review Completed**  
**Committee review completed.**

**General Schedule available**

**Bill of materials available (Finalized)**

**Requirements Document in progress**

**Detailed schedule in progress**

# Key Issues (Tasks)

Responsibility	Project Engineer (Roy Emerson)	Steve Ross	John Servino	Mike Fagan	Tom Dawson	Karen Walker	Tod Hayden (ECT)	Comments	Percent Complete
DIW Design	5	1	3	3	3	3		Package	60
DIW Electrical	5	1						BOM Done	
FERDP Design	5	1	3	3	3	3	3	Docs	20
FERDP Electrical	5	1						Changing Active Task	
Mezzanine Design	5	1	3	3	3	3			
Mezzanine Electrical	5	1						Layouts Done	80
Station Panel Design	5	1	3	3	3	3			10
Station Panel Electrical	5	1							
Printed Circuit Board Design	5	1				3	3		85
Printed Circuit Board Electrical	5	1							
Front-End Simulator Design	5	1	3	3	3	3	3		
Front-End Simulator Electrical	5	1							
Manual Test Box Design	5	1	3	3	3	3	3		
Manual Test Box Electrical	5	1							

## Legend

1 – Task designer -primary  
4 – Task approver

2 – Task supervisor  
5 – Supervisory oversight

3 – Task resource  
6 - Must be notified

## **Generation-3 General Schedule**

- **December 2002 - Start discussions about a Generation-3 PSS.**
- **March 2003 - Generation-3 presentation at CAT TWG meeting**
- **April 2003 - System architecture review**
- **June 2003 - Release (SOW) Statement of Work.**
- **July 2003 to**
- **January 2004 - Prepare documents and designs for necessary technology reviews and then construction.**
- **February 2004 - Start Installation at 30-ID, continue documentation.**
- **April 2004 - Validate system at 30-ID.**
- **May 2004 to**
- **September 2004 - Gain operational experience with the Generation-3 system.**
- October 2004 - Release Generation-3 system to CAT.**

# Resources

Responsibility	ASD Division Director (Rod Gerig)	PSS Gen-3 Review Committee	ES Group Leader (John Carwardine)	Project Engineer (Roy Emerson)	Steve Ross	John Servino	Mike Fagan	Tom Dawson	Anne Boron	Mariana Varotto	Van Nguyen	APS Radiation Safety Committee	Percent Complete
<b>Base Line Documents</b>													
SOW	4	6	4,5	1	6	3,6	3,6						100
Project Schedule	4	6	4,5	1	3,6	3,6	3,6						95
Requirements Specification	4	6	4	5	6	3,6	1						90
Functional Description	4		4	5		3,6	1						70
Beamline Configuration Spec	4		4	5		1	3,6						100
Goals Compliance Matrix			4	1									
<b>Interface Control Documents</b>													
ACIS interface to PSS			4	5			1						50
Front End & Beamline shutters			4	5			1						90
FE-EPS interface to PSS			4	5			1						100
BL-EPS interface to PSS			4	5			1						100
DIW interface to PSS			4	5			1						80
EPICS interface to PSS			4	5			1						50
HMI interface to PSS			4	5			1						75
<b>Hardware Design</b>													
New FERDP Design			4	5,6	1		3						10
PSS Mezzanine Design			4	5,6	1	3	3						80
PSS Station Design			4	5,6	1	3	3						100
Printed Circuit Boards			4	5,6	1								2
Front-End Simulator			4	5,6	1								2
Manual Test Box			4	5,6	1								2

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Note: Tasks assigned to Mariana Varotto are now to be provided by Van Nguyen.

## Resources - Continued

Responsibility	ASD Division Director (Rod Gerig)	PSS Gen-3 Review Committee	ES Group Leader (John Carwardine)	Project Engineer (Roy Emerson)	Steve Ross	John Servino	Mike Fagan	Tom Dawson	Anne Boron	Mariana Varotto	Van Nguyen	APS Radiation Safety Committee	Percent Complete
<b>Software Design</b>													
Chain-A ESD Code				5		6	6		1				
Chain-B ESD Code				5		6	6			1			
Chain-C C&C Code				5		1	6		6	6			
HMI Interface				5		1	6		6	6			50
EPICS Interface				1		6	6		6	6			90
<b>LAB Prototype Beamline</b>													
User Requirements Specification			4	6		6	6		1		6		
Input / Output & Fault List				6		1	6		6	6	6		95
HMI Specification				6	6	1	6						70
EPICS Specification				1		6	6		6	6			70
Validation Procedure				6					6	6	1		
PLC Programming Chain-A/B/C				6		1	6		1	1			
HMI Programming				6		1	6		6	6			
EPICS Programming				1					6	1,6			
Fabrication			4	6	1								

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